

Ţ		An apparatus for encrypting an identifier, the
2	apparatus co	omprising:
3		a pad for entering an identifier;
4		a circuit, adjacent the pad, for encrypting the entered
5	ident	ifier; and
6		$\ensuremath{\mathtt{a}}$ link, communicatively coupling the pad and the encrypting
7	circu <sup>-</sup>	it.
1		2. The apparatus of claim $1$ ,
2	wherein the	pad comprises
3		a touch pad.
1		3. The apparatus of claim 2,
2	wherein the	touch pad comprises
3		an N-wire-technology touch pad.
	•	
1		4. The apparatus of claim 2,
2	wherein the	touch pad comprises
3		a four-wire-technology touch pad.
1		5. The apparatus of claim 2,
2 ·	wherein the	touch pad comprises
3	WINCE CHE CHE	a seven-wire-technology touch pad.
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1		6. The apparatus of claim 1,
2	wherein the	pad comprises
3		a touch screen.

The apparatus of claim 1,

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2	wherein the	e pad comp	prises
3		a pad fo	or entering a personal identifier (PIN).
1		8. Ti	he apparatus of claim $oldsymbol{1}$ , wherein the encrypting
2	circuit com	prises	·
3		a CPU; a	and
4		a memory	y, coupled to the CPU and programmed to encrypt.
1	•	9. TI	ne apparatus of claim 8, wherein the CPU and
2	programmed	memory a	re the first CPU, programmable to encrypt the entered
3	identifier,	through	which the identifier passes.
			•
1		10. TI	ne apparatus of claim $oldsymbol{1}$ , wherein the encrypting
2	circuit com	prises	
3		a micro	controller programmed to encrypt.
1		11. Ti	ne apparatus of claim $oldsymbol{1}$ , wherein the encrypting
2	circuit com	prises	
3			ication-specific integrated circuit (ASIC).
1		12. TI	ne apparatus of claim $oldsymbol{1}$ , further comprising
2			ng enclosing the encrypting circuit and link and
3	resis	tant to a	
Ū	10313	cuite to t	
1		13. TI	ne apparatus of claim 12, wherein the housing
2	comprises		apparatus er era m. 22, merem ene mede mg
3	00p. 1000	housina	resistant to tampering.
Ū		11000 1119	Too too all of campo. Mg.
1		14. TI	ne apparatus of claim 12, wherein the housing
2	comprises		to apparation of the man, and the first find the
3	50mp1 1565	housing	resistant to tapping.
5		nous mg	100100ano oo oapping.

1 2	comprises	15. The apparatus of claim 12, wherein the housing		
3		housing at least partially of chip-on-glass technology.		
1		16. The apparatus of claim 12, wherein the housing		
2	comprises	housing in which the encrypting circuit is embedded.		
1 2	comprises	17. The apparatus of claim 12, wherein the housing		
3		housing in which the link and encrypting circuit are		
4	embedded.			
1		18. An apparatus for encrypting an identifier, the		
2	apparatus comprising:			
3		a pad, comprising one of a touch screen and an N-wire-		
4	technology touch pad, for entering a personal identifier (PIN);			
5		a circuit, adjacent the pad and comprising one of a		
6	programmed microcontroller and an ASIC, for encrypting the entered			
7	identifier;			
8		a link, communicatively coupling the pad and the encrypting		
9	circuit: and			
10		a housing, resistant to access and at least partially of		
11	chip-on-glass technology, in which the link and encrypting circui			
12	are e	embedded.		
1		19. A method for encrypting an identifier, the method		
2	comprising:			
3		placing a		
4		pad for entering an identifier.		

5	a circuit for encrypting an identifier and		
6	a link communicatively coupling the pad and the		
7	encrypting circuit		
8	adjacent in an access-resistant housing;		
9	entering a identifier on the pad:		
10	communicating the identifier to the encrypting circuit; and		
11	encrypting the identifier by means of the encrypting circuit		
1	20. The method of claim $19$ , further comprising the step of		
2	forwarding the encrypted identifier for verification.		
1	<b>21.</b> An apparatus for encrypting an identifier, the		
2	apparatus comprising:		
3	a pad for entering an identifier;		
4	a circuit for encrypting the entered identifier, the circuit		
5	being the first circuit receiving and programmable or designed to		
6	encrypt the entered identifier;		
7	a link, communicatively coupling the pad and the encrypting		
8	circuit; and		
9	a housing, shielding the link and circuit from physical		
10	access.		
1	22. The apparatus of claim $21$ , wherein the circuit		
2	•		
	·		
1 2 3	22. The apparatus of claim <b>21</b> , wherein the circuit comprises a circuit adjacent the pad.		